



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Nathan R. Brown

Serial No.: 10/715,267

Filed: November 17, 2003

For: METHODS FOR POLISHING
SEMICONDUCTOR DEVICE
STRUCTURES BY DIFFERENTIALLY
APPLYING PRESSURE TO SUBSTRATES
THAT CARRY THE SEMICONDUCTOR
DEVICE STRUCTURES

Confirmation No.: 4590

Examiner: S. Macarthur

Group Art Unit: 1763

Attorney Docket No.: 2269-4375.1US
(99-1029.01US)

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

January 5, 2006
Date


Signature

Erika Gandre
Name (Type/Print)

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This Amendment is being filed following the rejections presented in the Office Action mailed on October 5, 2005, the three-month shortened statutory period for response to which expires on January 5, 2005.

A listing of the claims, in which amendments to the claims are presented, starts at page 2 of this paper; and

Remarks begin on page 7 of this paper.